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-	31	(fuzzy and union and intersection and circuit) and @pd<=19981130	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/08 19:52
-	1041	(fuzzy and min and max and circuit) and @pd<=19981130	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/08 19:52
-	27	((fuzzy and min and max and circuit) and @pd<=19981130) and subtracter	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/08 19:52
-	213	fuzzy and union and circuit	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/05/08 19:53
-	94	(fuzzy and union and circuit) and intersection	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2003/09/30 16:57
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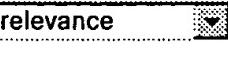
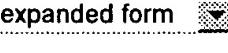
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January 1972 **Journal of the ACM (JACM)**, Volume 19 Issue 1

Full text available:  pdf(655.93 KB) Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)



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Francesco Marcelloni, Mehmet Aksit

December 2000 **ACM SIGAPP Applied Computing Review**, Volume 8 Issue 2

Full text available:  pdf(904.41 KB) Additional Information: [full citation](#), [abstract](#), [index terms](#)



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Justin Ammerlaan, David Wright

January 2004 **Proceedings of the 27th conference on Australasian computer science - Volume 26**

Full text available:  pdf(324.34 KB) Additional Information: [full citation](#), [abstract](#), [references](#)



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November 1992 **Proceedings of the conference on European design automation**

Full text available:  pdf(499.34 KB) Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)

**14 Teaching principles of fuzzy logic analysis using the BK-products model**

Bobby C. Granville

April 2003 **The Journal of Computing in Small Colleges**, Volume 18 Issue 4

Full text available:  pdf(44.13 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

The objective of the course is to give students experience using a generic front-end for relational knowledge based systems that explore the advantages of fuzzy "computing with words," a natural language protocol that provides users the freedom to permit their common use of vague measurement terms in analyzing vague and partial relational knowledge structures. This course applies fuzzy logic to filter, organize, and compare verbal relational requests (queries) that relate patient symptoms with d ...

**15 Implementation of fuzzy logic and neural networks control algorithm using a digital signal processing chip**

Kishan Kumar Kumbla, Mohammad Jamshidi, Jorge Benitez-Read

February 1995 **Proceedings of the 1995 ACM symposium on Applied computing**

Full text available:  pdf(453.87 KB) Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#)

**Keywords:** digital signal processor, fuzzy logic, inverse kinematics, neural networks, self-organizing controller

**16 Fuzzy logic, neural networks, and soft computing**

Lotfi A. Zadeh

March 1994 **Communications of the ACM**, Volume 37 Issue 3

Full text available:  pdf(2.17 MB) Additional Information: [full citation](#), [references](#), [citations](#), [index terms](#), [review](#)

**17 What does fuzzy logic bring to AI?**

Didier Dubois, Henri Prade

September 1995 **ACM Computing Surveys (CSUR)**, Volume 27 Issue 3

Full text available:  pdf(278.65 KB) Additional Information: [full citation](#), [references](#), [index terms](#), [review](#)

**18 FLAMES: A Fuzzy Logic ATMS and Model-based Expert System for Analog Diagnosis**

F. Mohamed, M. Marzouki, M. H. Touati

March 1996 **Proceedings of the 1996 European conference on Design and Test**

Full text available:  pdf(609.47 KB) Additional Information: [full citation](#), [abstract](#)  


Diagnosing analog circuits with their numerous known difficulties is a very hard problem. Digital approaches have proven to be inappropriate, and AI-based ones suffer from many problems. In this paper we present a new system, FLAMES, which uses fuzzy logic, model-based reasoning, ATMS extension, and the human expertise in an appropriate combination to go far in the treatment of this problem.

**19 A predictive self-tuning fuzzy-logic feedback rate controller**

Rose Qingyang Hu, David W. Petr

December 2000 **IEEE/ACM Transactions on Networking (TON)**, Volume 8 Issue 6

Full text available:  pdf(281.19 KB) Additional Information: [full citation](#), [references](#)

**Keywords:** adaptive control, asynchronous transfer mode, computer network performance, feedback systems, fuzzy control, predictive control, traffic control

**20 Hierarchical approach to fuzzy logic control**

Reza Langari

February 1996 **Proceedings of the 1996 ACM symposium on Applied Computing**

Full text available:  pdf(296.48 KB) Additional Information: [full citation](#), [references](#), [index terms](#)

**Keywords:** fuzzy logic, intelligent control

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November 1992 **Proceedings of the conference on European design automation**

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**14 Teaching principles of fuzzy logic analysis using the BK-products model**

Bobby C. Granville

April 2003 **The Journal of Computing in Small Colleges**, Volume 18 Issue 4

Full text available:  pdf(44.13 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)



The objective of the course is to give students experience using a generic front-end for relational knowledge based systems that explore the advantages of fuzzy "computing with words," a natural language protocol that provides users the freedom to permit their common use of vague measurement terms in analyzing vague and partial relational knowledge structures. This course applies fuzzy logic to filter, organize, and compare verbal relational requests (queries) that relate patient symptoms with d ...

**15 Implementation of fuzzy logic and neural networks control algorithm using a digital signal processing chip**

Kishan Kumar Kumbla, Mohammad Jamshidi, Jorge Benitez-Read

February 1995 **Proceedings of the 1995 ACM symposium on Applied computing**

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**Keywords:** digital signal processor, fuzzy logic, inverse kinematics, neural networks, self-organizing controller

**16 Fuzzy logic, neural networks, and soft computing** 

Lotfi A. Zadeh

March 1994 **Communications of the ACM**, Volume 37 Issue 3

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**17 What does fuzzy logic bring to AI?** 

Didier Dubois, Henri Prade

September 1995 **ACM Computing Surveys (CSUR)**, Volume 27 Issue 3

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**18 FLAMES: A Fuzzy Logic ATMS and Model-based Expert System for Analog Diagnosis** 

F. Mohamed, M. Marzouki, M. H. Touati

March 1996 **Proceedings of the 1996 European conference on Design and Test**

Full text available:  pdf(609.47 KB) Additional Information: [full citation](#), [abstract](#)  
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Diagnosing analog circuits with their numerous known difficulties is a very hard problem. Digital approaches have proven to be inappropriate, and AI-based ones suffer from many problems. In this paper we present a new system, FLAMES, which uses fuzzy logic, model-based reasoning, ATMS extension, and the human expertise in an appropriate combination to go far in the treatment of this problem.

**19 A predictive self-tuning fuzzy-logic feedback rate controller** 

Rose Qingyang Hu, David W. Petr

December 2000 **IEEE/ACM Transactions on Networking (TON)**, Volume 8 Issue 6

Full text available:  pdf(281.19 KB) Additional Information: [full citation](#), [references](#)

**Keywords:** adaptive control, asynchronous transfer mode, computer network performance, feedback systems, fuzzy control, predictive control, traffic control

**20 Hierarchical approach to fuzzy logic control** 

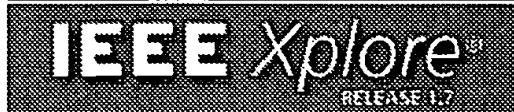
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*Patyra, M.J.; Long, J.E.;*  
Circuits and Systems, 1994. ISCAS '94., 1994 IEEE International Symposium on , Volume: 4 , 30 May-2 June 1994  
Pages:283 - 286 vol.4

[\[Abstract\]](#)   [\[PDF Full-Text \(344 KB\)\]](#)   IEEE CNF**2 CMOS fuzzy logic controller in current mode***Lemaitre, L.; Patyra, M.J.; Mlynek, D.;*  
Custom Integrated Circuits Conference, 1993., Proceedings of the IEEE 1993 May 1993  
Pages:25.8.1 - 25.8.3[\[Abstract\]](#)   [\[PDF Full-Text \(292 KB\)\]](#)   IEEE CNF**3 Programmable fuzzy logic controller circuit on CPLD chip***Samman, F.A.; Syamsuddin, E.Y.;*  
Circuits and Systems, 2002. APCCAS '02. 2002 Asia-Pacific Conference on , Volume: 2 , 28-31 Oct. 2002  
Pages:561 - 564 vol.2[\[Abstract\]](#)   [\[PDF Full-Text \(422 KB\)\]](#)   IEEE CNF**4 A fuzzy membership function circuit using hysteretic resonant tunn diodes***Hao Tang; Hung Chang Lin;*  
Multiple-Valued Logic, 1995. Proceedings., 25th International Symposium on 25 May 1995  
Pages:182 - 186

---

[Abstract] [PDF Full-Text (348 KB)] IEEE CNF

**5 A fuzzy partitioning system**

*Rao Valavala, K.; Manzoul, M.A.;*  
Micro, IEEE , Volume: 15 , Issue: 6 , Dec. 1995  
Pages:66

---

[Abstract] [PDF Full-Text (92 KB)] IEEE JNL

**6 Hardware fuzzy logic kit design**

*Gharieb, W.;*  
Fuzzy Systems, 1997., Proceedings of the Sixth IEEE International Conference , Volume: 2 , 1-5 July 1997  
Pages:1039 - 1043 vol.2

---

[Abstract] [PDF Full-Text (364 KB)] IEEE CNF

**7 A fault model and a test method for analog fuzzy logic circuits**

*Weiner, S.;*  
Test Conference, 1995. Proceedings., International , 21-25 Oct. 1995  
Pages:282 - 291

---

[Abstract] [PDF Full-Text (876 KB)] IEEE CNF

**8 A fuzzy logic inference processor**

*Fattaruso, J.W.; Mahant-Shetti, S.S.; Brock Barton, J.;*  
Industrial Fuzzy Control and Intelligent Systems, 1993., IFIS '93., Third International Conference on , 1-3 Dec. 1993  
Pages:210 - 214

---

[Abstract] [PDF Full-Text (316 KB)] IEEE CNF

**9 Synthesis and design automation of analog fuzzy logic VLSI circuits**

*Lemaitre, L.; Patyra, M.J.; Mlynek, D.;*  
Multiple-Valued Logic, 1993., Proceedings of The Twenty-Third International Symposium on , 24-27 May 1993  
Pages:74 - 79

---

[Abstract] [PDF Full-Text (540 KB)] IEEE CNF

**10 Piecewise linear macromodels for elementary logic and fuzzy circuits**

*Tesu, I.C.; Dartu, F.;*  
Circuits and Systems, 1993., ISCAS '93, 1993 IEEE International Symposium on , 3-6 May 1993  
Pages:1718 - 1721 vol.3

---

[Abstract] [PDF Full-Text (276 KB)] IEEE CNF

**11 Fuzzy-logic-based analog design tools**

*Torralba, A.J.; Chavez, J.; Franquelo, L.G.;*  
Micro, IEEE , Volume: 16 , Issue: 4 , Aug. 1996  
Pages:60 - 68

---

[\[Abstract\]](#) [\[PDF Full-Text \(684 KB\)\]](#) [IEEE JNL](#)

---

**12 Efficient fuzzy logic architectures suitable for silicon compilation**

*Wicks, T.; Nigri, M.; Treleaven, P.;*

Proceedings of ISUMA - NAFIPS '95 The Third International Symposium on Uncertainty Modeling and Analysis and Annual Conference of the North American Fuzzy Information Processing Society , 17-20 Sept. 1995

Pages:363 - 368

---

[\[Abstract\]](#) [\[PDF Full-Text \(532 KB\)\]](#) [IEEE CNF](#)

---

**13 Application of fuzzy logic in resistive fault modeling and simulation**

*Nourani, M.; Attarha, A.R.; Lucas, C.;*

Fuzzy Systems, IEEE Transactions on , Volume: 10 , Issue: 4 , Aug. 2002

Pages:461 - 472

---

[\[Abstract\]](#) [\[PDF Full-Text \(373 KB\)\]](#) [IEEE JNL](#)

---

**14 Analysis and design of CMOS fuzzy logic controller in current mode**

*Lemaitre, L.; Patyra, M.J.; Mlynek, D.;*

Solid-State Circuits, IEEE Journal of , Volume: 29 , Issue: 3 , March 1994

Pages:317 - 322

---

[\[Abstract\]](#) [\[PDF Full-Text \(520 KB\)\]](#) [IEEE JNL](#)

---

**15 Analog gates for a VLSI fuzzy processor**

*Catania, V.; Russo, M.;*

VLSI Design, 1995., Proceedings of the 8th International Conference on , 4-7 1995

Pages:299 - 304

---

[\[Abstract\]](#) [\[PDF Full-Text \(544 KB\)\]](#) [IEEE CNF](#)

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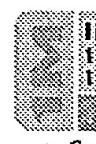
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Arabshahi, P.; Marks, R.J., II; Seho Oh; Caudell, T.P.; Choi, J.J.; Bong-Gee S  
Circuits and Systems II: Analog and Digital Signal Processing, IEEE Transactions on [see also Circuits and Systems II: Express Briefs, IEEE Transactions on] , Vol 44 , Issue: 9 , Sept. 1997  
Pages:696 - 709

[Abstract]   [\[PDF Full-Text \(500 KB\)\]](#)   [IEEE JNL](#)

#### 2 Design and preliminary results of high speed analog 1.0 μm CMOS MIN/MAX circuit for fuzzy architectures

Gabrielli, A.; Gandolfi, E.; Masetti, M.; Maloberti, F.;  
Circuits and Systems, 1995., Proceedings., Proceedings of the 38th Midwest Symposium on , Volume: 1 , 13-16 Aug. 1995  
Pages:381 - 384 vol.1

[Abstract]   [\[PDF Full-Text \(348 KB\)\]](#)   [IEEE CNF](#)

#### 3 Design of MIN/MAX cellular neural networks (MMCNNs) in CMOS technology

Wen-Cheng Yen; Rong-Jian Chen; Jui-Lin Lai;  
Cellular Neural Networks and Their Applications, 2002. (CNNA 2002). Proceedings of the 2002 7th IEEE International Workshop on , 22-24 July 2002  
Pages:339 - 346

[Abstract]   [\[PDF Full-Text \(340 KB\)\]](#)   [IEEE CNF](#)

#### 4 Fuzzy aggregating functions for multiobjective VLSI placement

Khan, J.A.; Sait, S.M.;

Fuzzy Systems, 2002. FUZZ-IEEE'02. Proceedings of the 2002 IEEE International Conference on , Volume: 2 , 12-17 May 2002  
Pages:831 - 836

[\[Abstract\]](#) [\[PDF Full-Text \(548 KB\)\]](#) [IEEE CNF](#)

---

**5 The concept of fuzzy flip-flop**

*Hirota, K.; Ozawa, K.;*  
Systems, Man and Cybernetics, IEEE Transactions on , Volume: 19 , Issue: 5 , Sept.-Oct. 1989  
Pages:980 - 997

[\[Abstract\]](#) [\[PDF Full-Text \(888 KB\)\]](#) [IEEE JNL](#)

---

**6 The synthesis of compact fuzzy neural circuits**

*Hurdle, J.F.;*  
Fuzzy Systems, IEEE Transactions on , Volume: 5 , Issue: 1 , Feb. 1997  
Pages:44 - 55

[\[Abstract\]](#) [\[PDF Full-Text \(200 KB\)\]](#) [IEEE JNL](#)

---

**7 Evaluation of min/max instructions for fuzzy information processing**

*Watanabe, H.; Chen, D.; Konuri, S.;*  
Fuzzy Systems, IEEE Transactions on , Volume: 4 , Issue: 3 , Aug. 1996  
Pages:369 - 374

[\[Abstract\]](#) [\[PDF Full-Text \(472 KB\)\]](#) [IEEE JNL](#)

---

**8 Fuzzy Petri nets for rule-based pattern classification**

*Xi Chen; Dongming Jin; Zhijian Li;*  
Communications, Circuits and Systems and West Sino Expositions, IEEE 2002 International Conference on , Volume: 2 , 29 June-1 July 2002  
Pages:1218 - 1222 vol.2

[\[Abstract\]](#) [\[PDF Full-Text \(367 KB\)\]](#) [IEEE CNF](#)

---

**9 Recursive training for multi-resolution fuzzy min-max neural network classifier**

*Chen Xi; Jin Dongming; Li Zhijian;*  
Solid-State and Integrated-Circuit Technology, 2001. Proceedings. 6th International Conference on , Volume: 1 , 22-25 Oct. 2001  
Pages:131 - 134 vol.1

[\[Abstract\]](#) [\[PDF Full-Text \(230 KB\)\]](#) [IEEE CNF](#)

---

**10 Mixed-signal CMOS fuzzifier with emphasis on power consumption**

*Carvajal, R.G.; Torralba, A.; Colodro, F.; Franquelo, L.G.;*  
Circuits and Systems, 1999. 42nd Midwest Symposium on , Volume: 2 , 8-11 1999  
Pages:929 - 933 vol. 2

[\[Abstract\]](#) [\[PDF Full-Text \(284 KB\)\]](#) [IEEE CNF](#)

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**11 Hardware fuzzy logic kit design***Gharieb, W.;*

Fuzzy Systems, 1997., Proceedings of the Sixth IEEE International Conference , Volume: 2 , 1-5 July 1997  
Pages:1039 - 1043 vol.2

[\[Abstract\]](#) [\[PDF Full-Text \(364 KB\)\]](#) [IEEE CNF](#)**12 A reconfigurable parallel inference processor for high speed fuzzy systems***Lees, M.J.; Campbell, D.A.; Devlin, J.C.;*

Circuits and Systems, 1996. ISCAS '96., 'Connecting the World',, 1996 IEEE International Symposium on , Volume: 3 , 12-15 May 1996  
Pages:539 - 542 vol.3

[\[Abstract\]](#) [\[PDF Full-Text \(248 KB\)\]](#) [IEEE CNF](#)**13 Continuous-time analog defuzzifier for product-sum based implementations***Rojas, I.; Pelayo, F.J.; Anguita, M.; Prieto, A.;*

Microelectronics for Neural Networks and Fuzzy Systems, 1994., Proceedings Fourth International Conference on , 26-28 Sept. 1994  
Pages:324 - 330

[\[Abstract\]](#) [\[PDF Full-Text \(368 KB\)\]](#) [IEEE CNF](#)**14 Architecture of a 50 MFIPS fuzzy processor and the related 1 μm V CMOS digital circuits***Gandolfi, E.; Masetti, M.; D'Antone, I.; Gabrielli, A.; Spotti, M.;*

Microelectronics for Neural Networks and Fuzzy Systems, 1994., Proceedings Fourth International Conference on , 26-28 Sept. 1994  
Pages:125 - 133

[\[Abstract\]](#) [\[PDF Full-Text \(632 KB\)\]](#) [IEEE CNF](#)**15 Implementation of O(n) complexity max/min circuits for fuzzy and connectionist computing***Hassoun, M.H.; Nabha, A.M.;*

Neural Networks, 1993., IEEE International Conference on , 28 March-1 April  
Pages:998 - 1003 vol.2

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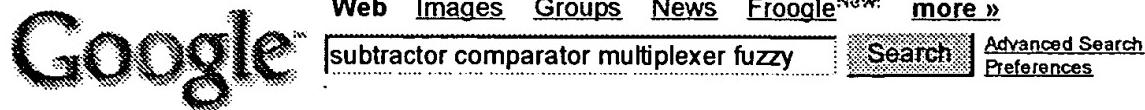


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1 DETIK LESS THAN **COMPARATOR B A** ... The **multiplexer** 3 to 1 selects one of three ...[www.tec.puv.fi/~smv/ED1/PROJECT/xs\\_fuzzy.pdf](#) - Supplemental Result - [Similar pages](#)**[PDF] VOLWASSENENONDERWIJS Technisch**File Format: PDF/Adobe Acrobat - [View as HTML](#)... 2-bit **comparator** • 7.3 4-bit **comparator** • 7.4 74xx85 magnitude **comparator** 8 - de ... 8 **Multiplexer** en demultiplexer • 8.1 4 naar 1 **multiplexer** • 8.2 1 naar ...[www.gemeenschapsonderwijs.be/pbd/vwo/leerplannen/2003-796L.pdf](#) - [Similar pages](#)**ರכש אינטרנט**מעגלים - ADDER & **SUBTRACTOR** ... ICS, **COMPARATOR**, VOLTAGE... מעגלים משלבים ... ICS, **MULTIPLEXER (DIGITAL)** - ... משלבים[www.hi-tech.co.il/rechesh/ManufacturerInfo.asp?ManufacturerName=KTR-TECH%20LTD.&RepId=281](#) - 93k - Cached - [Similar pages](#)**[PDF] F03 E15/CS24 Lecture #1**File Format: PDF/Adobe Acrobat - [View as HTML](#)... B+ standard Syllabus • Textbook: Digital Design with VHDL, Neuro-Fuzzy and Soft ... to allow hierachical design Simple Example 1: A **comparator** entity compare is ...[palantir.swarthmore.edu/maxwell/classes/e15/F03/E15\\_F03\\_Lectures.pdf](#) - [Similar pages](#)**[PDF] Implementation of Autonomous Fuzzy Garage-Parking Control by an ...**File Format: PDF/Adobe Acrobat - [View as HTML](#)... value, then the output of the **comparator** exports '0 ... FLC module, we use the **multiplexer** as the ... remainder register, quotient register, **subtractor** register, and ...[d-ip-129-15-98-114.enrgr.ou.edu/~dmiller/courses/ame5750/robot%20papers/I CRA03/pdf/3776.pdf](#) - Supplemental Result - [Similar pages](#)**[PDF] SCHEME OF INSTRUCTION AND EXAM. B.Tech. (Instrumentation ...**File Format: PDF/Adobe Acrobat - [View as HTML](#)

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... Adder and **Subtractor** Constant Voltage and Current Circuits Differentiator and Integrator

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... systems. - MSI devices - adder, **subtractor**, **comparator**, **multiplexer** and decoder devices. - Sequential devices - registers, counters. ...

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... operators that can be inferred by PL Synthesizer are: Adder, Adder/ **Subtractor**, **Comparator**, Counter, Decrementor, Incrementor, **Multiplexer**, Multiplier, Registers ...

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### [Assignment 2](#)

... Multiplexer, 24 gates. 2 levels. Ripple-Carry **Subtractor**, 46 gates. 11levels. Carry-Lookahead **subtractor**, 124 gates. 3 levels. Less Than **Comparator**, 59 gates. 3 levels. ...

[www.ee.pdx.edu/~mperkows/CLASS\\_VHDL/VHDL/gcd/assignme.htm](http://www.ee.pdx.edu/~mperkows/CLASS_VHDL/VHDL/gcd/assignme.htm) - 23k - [Cached](#) - [Similar pages](#)

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... Digital Gates, Adder, **Subtractor**, Multiplier, Decoder, Encoder, Magnitude **Comparator**, **Multiplexer** and Demultiplexer in Graphics using C programming language. ...

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... Components in C Programming Language: Digital Gates, Adder, **Subtractor**, Multiplier, Decoder, Encoder, Magnitude **Comparator**, **Multiplexer** and Demultiplexer in ...

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### [FPU](#)

... Adder/Subtractor, 24-bit **Comparator**, 8-bit **Comparator**, 24-bit Shifter, and 24-bit Priority Encoder. This is in addition to more than 10 different **multiplexer** ...

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... 74LS385. Quad Serial Adder/Subtractor. 74LS386. ... 74LS399. Quad 2-In **Multiplexer** w/Output Register. 74LS490. ... 74LS682. 8-Bit Magnitude **Comparator** 3 State. 74LS683. ...

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... The logic for the look-ahead carry adder/subtractor, the **comparator** and the **multiplexer** can be found in any digital design textbook, eg (Mano 1991). ...

[homepages.inf.ed.ac.uk/rbf/CVonline/LOCAL\\_COPIES/GASTERATOS/iv.htm](http://homepages.inf.ed.ac.uk/rbf/CVonline/LOCAL_COPIES/GASTERATOS/iv.htm) - 101k - [Cached](#) - [Similar pages](#)

TTL Logic

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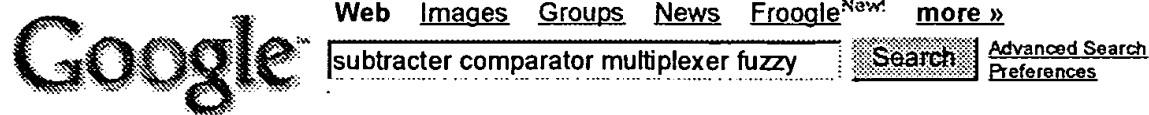
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• adder/subtractor/2-bit ... coder/decoder/multiplexer • register/counter ...

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**[ps] Chapter 5 Reconfigurable Analog-to-Digital Converter**

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... The **comparator** upon which this design is based exhibited offsets ... 54 5.5.3 D/A Converter and **Subtracter**. ... The digital bits control a **multiplexer** that selects the ...

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... 2004 Digital Logic Design 9 Parallel binary adder/subtracter JC Huang ... numbers A and B. (a) 1-bit **comparator** network ... D 5 D 6 D 7 8x1 **multiplexer** 1x8 demultiplexer ...

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... In this case only the **multiplexer** select pins are saved. ... A whole ALU including the adder, **subtracter**, **comparator** and logical operations consists of 41 CLBs. ...

[www.ida.ing.tu-bs.de/research/publications/ps/BEK+95:ProtoSysteVerifEvalu.ps](http://www.ida.ing.tu-bs.de/research/publications/ps/BEK+95:ProtoSysteVerifEvalu.ps) - Similar pages

**Answer Record # 8968: 3.1 i COREGEN: What CORE Generator IP modules ...**

... the following: Accumulator (V1.0.3) Adder **Subtracter** (V1.0.3) Asynchronous\_FIFO (V1.0.3)

**Comparator** (V1.0.3 ... V1.0.3) Bus Gate (V1.0.3) Bit **Multiplexer** (V1.0.3 ...

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..... 7 2.4.3 Bit **Comparator** Block ..... 10 2.4.8 2-1 **Multiplexer**

..... 11 2.4.10 Adder / **Subtracter** Block .....

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... Some combinational circuits like adder, **subtracter**, encoder, decoder, **comparator**, **multiplexer**, and parity generator. Elementary sequential circuits. ...

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... minimize the connectivity (to reduce measures like the **multiplexer** input count ... consisted of 2 multipliers, 1 adder, 1 **subtractor**, and 1 **comparator** that all ...

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... 2. Substitution scheme for a simplified algorithm characterisation Operation Substitution

**Subtractor Adder Comparator Adder Shifter Multiplexer Barrel-shifter ...**

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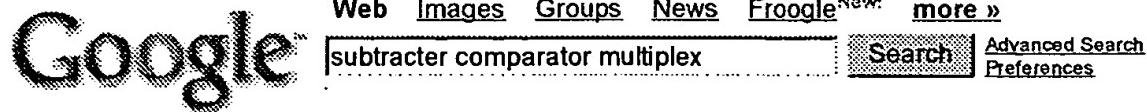
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... the delay of MUX Mux MUX Mux and MUX As seen ... case delay of ns for the combined **comparator** selector does ... delay is less than a combined adder **subtractor**'s delay ...

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... Drawer 144: Shift storage register, shifter, **MUX**, data selector, ... Adder/**subtractor**, Poly. ... Register. Drawer 145: DF/F, **Comparator**, DCOD, Buffer/line driver, ...

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12.6.9 Adders and Arithmetic Functions 603 12.6.10 Adder/Subtractor and Don ...

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... Register a **Comparator** Register s Register F Register r Register f **Subtracter Comparator**

Output Logic Rank Update ... b:K w=c: K 2 +3K-2 w=K:K Out Logic (**mux**): K S ...

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... sub : adder and **subtractor** asc : adder, **subtractor**, **comparator** dec : decrementer ... ahead and carry in sub : **subtractor** with c ... MAP(clock, reg.en(4), MUX.3.output.a ...  
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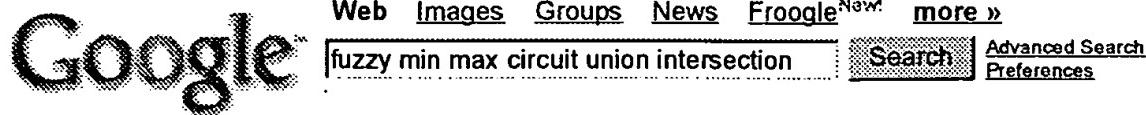
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